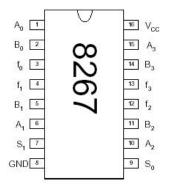
Signetics Chips used in the Mark 8

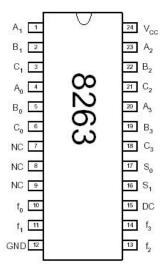
2-Input 4-BIT Digital Multiplexer



TRUTH TABLE

Select Lines		Outputs	
So	S ₁	f _n (0,1,2,3)	
0	0	В.,	
0	1	B _n	
1	0	A	
1	1	1	

3-Input 4-BIT Digital Multiplexer



TRUTH TABLE

DATA INPUT A _n B _n C _n		NNEL ECT S ₁	DATA COMPLEMENT	DATA OUTPUTS f _n (0,1,2,3)
A _n x x	1	1	0	An
x B _n x	0	1	0	B _n
x x C	1	0	0	C _n
x x x	.0	0	0	0
A _n x x	1	1	1	A,
x B _n x	0	1	1	ਲ,
x x C	1	0	1	표 C
x x x	0	0	1	1"
x x x	х	0 X	X	1